

REPETITIVE AVALANCHE AND dv/dt RATED HEXFET® TRANSISTOR

IRHM9064

P-CHANNEL RAD HARD

-60 Volt, 0.060Ω , RAD HARD HEXFET

International Rectifier's P-Channel RAD HARD technology HEXFETs demonstrate excellent threshold voltage stability and breakdown voltage stability at total radiation doses as high as 10⁵ Rads (Si). Under **identical** pre- and post-radiation test conditions. International Rectifier's P-Channel RAD HARD HEXFETs retain identical electrical specifications up to 1 x 10⁵ Rads (Si) total dose. No compensation in gate drive circuitry is required. These devices are also capable of surviving transient ionization pulses as high as 1 x 10¹² Rads (Si)/Sec, and return to normal operation within a few microseconds. Single Event Effect (SEE) testing of International Rectifier P-Channel RAD HARD HEXFETs has demonstrated virtual immunity to SEE failure. Since the P-Channel RAD HARD process utilizes International Rectifier's patented HEXFET technology, the user can expect the highest quality and reliability in the industry.

P-Channel RAD HARD HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits in space and weapons environments.

Product Summary

Part Number	BVDSS	RDS(on)	lb
IRHM9064	-60V	0.060Ω	-35*A

Features:

- Radiation Hardened up to 1 x 10⁵ Rads (Si)
- Single Event Burnout (SEB) Hardened
- Single Event Gate Rupture (SEGR) Hardened
- Gamma Dot (Flash X-Ray) Hardened
- Neutron Tolerant
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Ceramic Eyelets

Absolute Maximum Ratings

Pre-Radiation

	Parameter	IRHM9064	Units
ID @ VGS = -12V, TC = 25°C		-35*	
ID @ VGS = -12V, TC = 100°C	Continuous Drain Current	-26	Α
IDM	Pulsed Drain Current ①	-168	
P _D @ T _C = 25°C	Max. Power Dissipation	250	W
	Linear Derating Factor	2.0	W/K ⑤
Vgs	Gate-to-Source Voltage	± 20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
IAR	Avalanche Current ①	-35*	Α
EAR	Repetitive Avalanche Energy ①	25	mJ
dv/dt	Peak Diode Recovery dv/dt 3	-5.5	V/ns
TJ	Operating Junction	-55 to 150	
T _{STG}	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10s	°C
	Weight	9.3 (typical)	g

IRHM9064 Pre-Radiation

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min	Тур	Max	Units	Test Conditions		
BVDSS	Drain-to-Source Breakdown Voltage	-60	_	1	V	VGS =0 V, ID = -1.0mA		
ΔBV _{DSS} /ΔT _J	Temperature Coefficient of Breakdown Voltage	_	-0.048	1	V/°C	Reference to 25°C, I _D = -1.0mA		
RDS(on)	Static Drain-to-Source	_	_	0.060	_	Vgs = -12V, ID = -26A ④		
	On-State Resistance	_	_	0.070	Ω	VGS = -12V, ID = -35A		
VGS(th)	Gate Threshold Voltage	-2.0	_	-4.0	V	$V_{DS} = V_{GS}$, $I_{D} = -1.0$ mA		
9fs	Forward Transconductance	16	_	_	S (7)	V _{DS} > -15V, I _{DS} = -26 A ④		
IDSS	Zero Gate Voltage Drain Current	_	_	-25	μА	Vps= 0.8 x Max Rating,Vgs=0V		
		_	_	-250	μΑ	V _{DS} = 0.8 x Max Rating		
						VGS = 0V, TJ = 125°C		
IGSS	Gate-to-Source Leakage Forward	_	_	-100	- A	VGS =-20 V		
IGSS	Gate-to-Source Leakage Reverse	_	_	100	nA	VGS = 20V		
Qg	Total Gate Charge	_	_	260		VGS =-12V, ID = -35A		
Qgs	Gate-to-Source Charge	_	_	60	nC	V _{DS} = Max Rating x 0.5		
Q _{gd}	Gate-to-Drain ('Miller') Charge	_	_	86				
td(on)	Turn-On Delay Time	_	_	62		$V_{DD} = -30V, I_{D} = -35A,$		
t _r	Rise Time	_	_	227		$R_G = 2.35\Omega$		
^t d(off)	Turn-Off Delay Time	_	_	200	ns			
tf	Fall Time		_	115				
LD	Internal Drain Inductance	_	8.7		nH	Measured from drain lead, 6mm (0.25 in) from package to center of die. Modified MOSFET symbol showing the internal inductances.		
LS	Internal Source Inductance	_	8.7	_		Measured from source lead, 6mm (0.25 in) from package to source bonding pad.		
C _{iss}	Input Capacitance	_	7400	I		VGS = 0V, VDS = -25 V		
Coss	Output Capacitance	_	3200	_	pF	f = 1.0MHz		
C _{rss}	Reverse Transfer Capacitance	_	540					

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Тур	Max	Units	Test Conditions		
Is	Continuous Source Current (E	_	_	-35	Α	Modified MOSFET symbol		
ISM	Pulse Source Current (Body [_	_	-168		showing the integral reverse p-n junction rectifier.		
VSD	Diode Forward Voltage	_	_	-3.0	V	$T_j = 25$ °C, $I_S = -35$ A, $V_{GS} = 0$ V ④		
t _{rr}	Reverse Recovery Time	_	_	480	ns	$T_j = 25^{\circ}C$, $I_F = -35A$, $di/dt \le -100A/\mu s$		
QRR	Reverse Recovery Charge			_	3.7	μС	V _{DD} ≤ -50V ④	
ton	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by Lg + LD.						

Thermal Resistance

	Parameter	Min	Тур	Max	Units	Test Conditions
RthJC	Junction-to-Case	_	_	0.50		
R _{th} JA	Junction-to-Ambient	_	_	48	K/W ⑤	
RthCS	Junction-to-Sink	_	0.21	_		Typical socket mount

Radiation Performance of P-Channel Rad Hard HEXFETs

International Rectifier Radiation Hardened HEXFETs are tested to verify their hardness capability. The hardness assurance program at International Rectifier uses two radiation environments.

Every manufacturing lot is tested in a low dose rate (total dose) environment per MIL-STD-750, test method 1019. International Rectifier has imposed a standard gate voltage of -12 volts per note 6 and a V_{DSS} bias condition equal to 80% of the device rated voltage per note 7. Pre- and post-radiation limits of the devices irradiated to 1 x 10⁵ Rads (Si) are identical and are presented in Table 1. The values in Table 1 will be met for either of the two low dose rate test

circuits that are used. Both pre- and post-radiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison. It should be noted that at a radiation level of 1 x 10⁵ Rads (Si) no changes in limits are specified in DC parameters.

High dose rate testing may be done on a special request basis using a dose rate up to 1 x 10¹² Rads (Si)/Sec.

International Rectifier radiation hardened P-Channel HEXFETs are considered to be neutron-tolerant, as stated in MIL-PRF-19500 Group D. International Rectifier radiation hardened P-Channel HEXFETs have been characterized in heavy ion Single Event Effects (SEE) environments and the results are shown in Table 3.

Table 1. Low Dose Rate 6	7	IRHM9064
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	Parameter		ads (Si)	Units	Test Conditions ®
		Min	Max		
BV _{DSS}	Drain-to-Source Breakdown Voltage	-200	_	V	$V_{GS} = 0V, I_{D} = -1.0mA$
VGS(th)	Gate Threshold Voltage ④	-2.0	-4.0		$VGS = V_{DS}$, $I_D = -1.0 \text{mA}$
IGSS	Gate-to-Source Leakage Forward	_	-100	nA	V _{GS} = -20V
IGSS	Gate-to-Source Leakage Reverse	_	100		$V_{GS} = 20V$
IDSS	Zero Gate Voltage Drain Current	_	-25	μΑ	V _{DS} =0.8 x Max Rating, V _{GS} =0V
R _{DS(on)1}	Static Drain-to-Source ④	_	0.060	Ω	VGS = -12V, I _D = -26A
	On-State Resistance One				
V_{SD}	Diode Forward Voltage	_	-3.0	V	$TC = 25^{\circ}C$, $I_S = -35A$, $V_{GS} = 0V$

Table 2. High Dose Rate ®

		10 ¹¹ F	10 ¹¹ Rads (Si)/sec			Rads (ds (Si)/sec		
	Parameter	Min	Тур	Max	Min	Тур	Max	Units	Test Conditions
V _{DSS}	Drain-to-Source Voltage	_	_	-48	_	_	-48	V	Applied drain-to-source voltage during
									gamma-dot
IPP		_	-100	_	_	-100	_	Α	Peak radiation induced photo-current
di/dt		_	-800		_	-160		A/µsec	Rate of rise of photo-current
L ₁		0.1	_		0.8	l	l	μH	Circuit inductance required to limit di/dt

Table 3. Single Event Effects ©

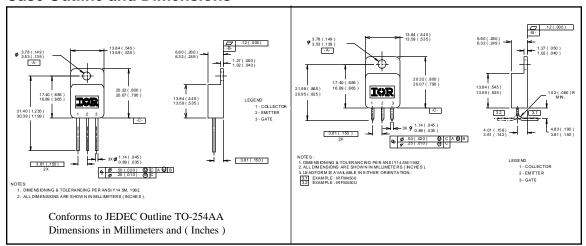
	Parameter	Typical	Units	lon	LET (Si) (MeV/mg/cm²)	Fluence (ions/cm²)	_	V _{DS} Bias (V)	V _{GS} Bias (V)
ı	BV _{DSS}	-60	V	Ni	28	1 x 10 ⁵	~41	-60	5

IRHM9064 Pre-Radiation

- Repetitive Rating; Pulse width limited by maximum junction temperature.
 Refer to current HEXFET reliability report.
- ② @ V_{DD} = -25V, Starting T_{J} = 25°C, EAS = [0.5 * L * (I_{L} 2) * [BVDSS/(BVDSS-VDD)] Peak I_{L} = -35A, V_{GS} = -12 V, 25 ≤ R_G ≤ 200 Ω
- ③ I_{SD} ≤ -35A, di/dt ≤-170 A/ μ s, V_{DD} ≤ BV_{DSS}, T_J ≤ 150°C Suggested RG = 2.35 Ω
- ④ Pulse width ≤ 300 μ s; Duty Cycle ≤ 2%
- ⑤ K/W = °C/W W/K = W/°C
- * Current is limited by Pin diameter

- © Total Dose Irradiation with VGS Bias. -12 volt VGS applied and VDS = 0 during irradiation per MIL-STD-750, method 1019.
- Total Dose Irradiation with Vpg Bias.
 Vpg = 0.8 rated BVpgg (pre-radiation)
 applied and Vgg = 0 during irradiation per
 MIL-STD-750, method 1019.
- This test is performed using a flash x-ray source operated in the e-beam mode (energy ~2.5 MeV), 30 nsec pulse.
- Process characterized by independent laboratory.
- All Pre-Radiation and Post-Radiation test conditions are identical to facilitate direct comparison for circuit applications.

Case Outline and Dimensions —



CAUTION BERYLLIA WARNING PER MIL-PRF-19500

Package containing beryllia shall not be ground, sandblashted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.



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